BRIEF DESCRIPTION OF THE DRAWINGS

Fig. 1 shows a block diagram of the embodiment example of the present invention. Fig. 2 shows a block diagram illustrating the idea of the prior art predistortion method. Fig. 3 shows a block diagram of one example of the distortion generator used for the circuit of Fig. 2. Fig. 4, Fig. 5, Fig. 6 (a), (b), (c) and Fig. 7 show characteristic graphs for illustrating the operation and effect of the present invention.

1. input signal

2, 3, 4, 5. branch circuit

6, 7, 8. delay line

9. subtraction circuit

10, 11, 12, 13, 14, 14a, 32. variable attenuator

15, 15a, 31. non-linear element

16. phase shifter

17, 18. adding circuit

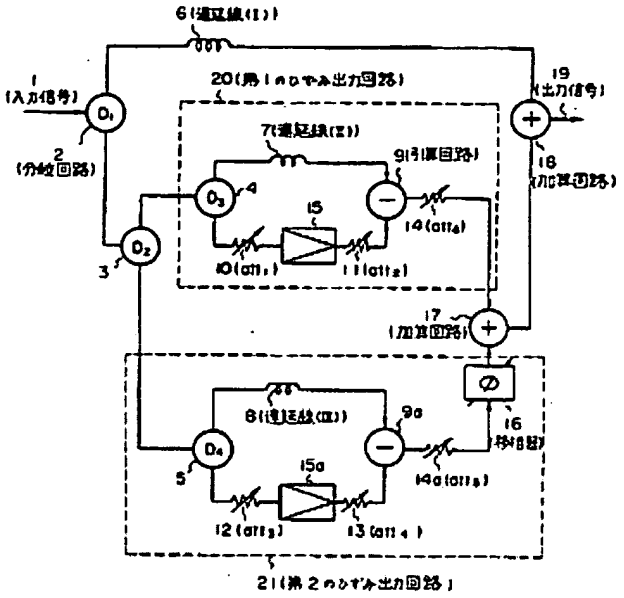
19. output signal

20. first distortion output circuit

21. second distortion output circuit

30. reverse distortion generation circuit

第 1 図



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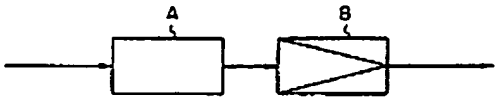
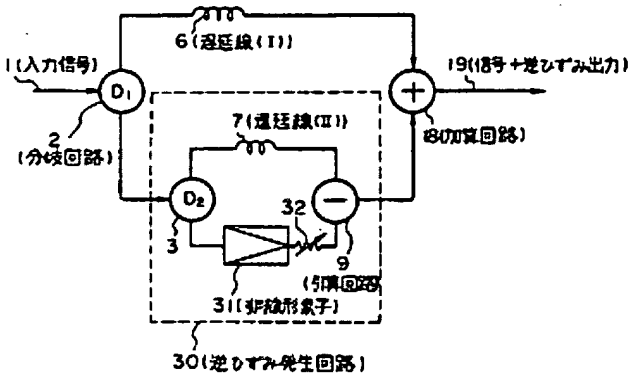


第 3 図



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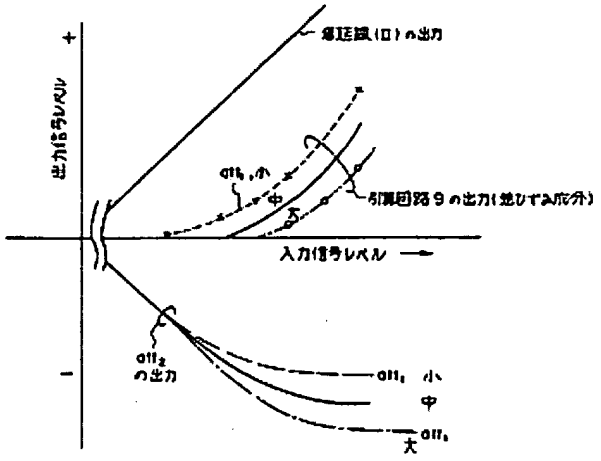

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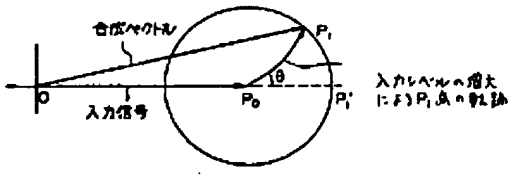
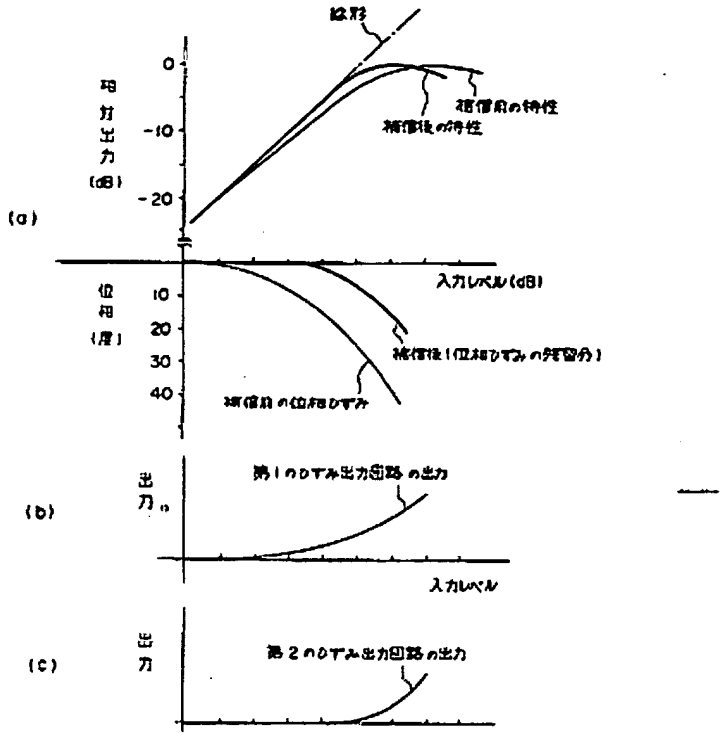


第 6 図



第 7 図